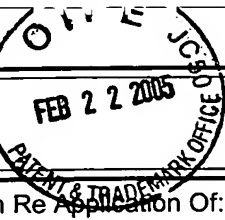


FEB 22 2005



TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
00750484AA

In Re Application Of: Doris et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/735,736	12/16/2003	Y. Huynh	30743	2813	3646

Title: **ULTRA-THIN SI CHANNEL CMOS WITH IMPROVED SERIES RESISTANCE**

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

**Response to Restriction Requirement
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Signature

Dated: February 22, 2005

Marshall M. Curtis
Reg. No. 33,138

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FIS920030123US1 (00750484AA)

PATENT APPLICATION

1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Bruce B. Doris et al.

Conf. No.: 3646

Serial No.: 10/735,736

Group Art Unit: 2813

Filed: December 16, 2003

Examiner: Y. Huynh

For: ULTRA-THIN SI CHANNEL CMOS WITH IMPROVED SERIES
RESISTANCE

Commissioner for Patents

United States Patent and Trademark Office

P. O. Box 1450

Alexandria, Virginia 22313-1450

RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

In response to the Office Action mailed January 27, 2005, Applicants provisionally elect the invention, as identified by the Examiner, of Group I, method claims 1 - 8, with traverse, for further action on the merits.

No amendments to the specification or claims are presented in this paper.